U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,010,511	04-1991	Hartley et al.	708/707
	B	US-6,192,384	02-2001	Dally et al.	708/200
	C	US-4,941,119	07-1990	Moline, Timothy W.	708/553
	D	US-4,943,908	07-1990	Emma et al.	712/240
	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ruby B. Lee; Efficiency of microSIMD architectures and index-mapped data for media processors; Media Processors 1999; January 25-29, 1999; Pages 34-46
	V	Jae-Woo Ahn and Wonyong Sung; Multimedia Processor-Based Implementaion of an Error-Diffusion Halftoning Algorithm Exploiting Subword Parrallelism; Circuits and Systems for Video Technology; Feb 2001; Pages 129-138
	W	Ruby B. Lee; Multimedia Extensions for General-Purpose Processors; Signal Processing Systems, 1997; November 3-5, 1997; Pages 9-23
	X	John L Hennessy and David A Patterson; Computer Organization and Design The Hardware/Software Interface; Morgan Kaufman Publishers; 1998; pages 221-223

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.